

IN THE CLAIMS

Please cancel claims 1-4 and 28-37 without prejudice.

Claims 1-4 (Cancelled)

5. (Original) A circuit array module, comprising:
 - a module body comprising a processor;
 - a configuration storage for storing a plurality of configuration definitions, wherein at least two of the plurality of configuration definitions define different configurations for the processor and wherein each configuration definition within the plurality of configuration definitions is associated with a specified identity;
 - an attribute detector for determining an attribute of the module body; and
 - a configuration selector for selecting a selected configuration definition from the plurality of configuration definitions depending on the attribute.
6. (Original) The circuit array module according to claim 5, wherein the attribute detector determines a serial number that is associated with the module body.
7. (Original) The circuit array module according to claim 5, wherein the attribute detector determines a module type of the module body.
8. (Original) The circuit array module according to claim 5, wherein the attribute detector determines at least one component that is available within the module body.
9. (Original) The circuit array module according to claim 5, wherein the processor comprises at least one of a programmable computer, a microprocessor, a micro-controller, a reduced instruction set computer and a digital signal processor.

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10. (Original) The circuit array module according to claim 5, wherein the processor comprises at least one of a field programmable gate array, an analog to digital converter and a digital to analog converter.
11. (Original) The circuit array module according to claim 5, wherein the module body comprises at least one of a cross-point switch, a memory device, a programmable termination network, user circuit connections, test equipment connections and a wiring interconnect.
12. (Original) The circuit array module according to claim 5, wherein the configuration selector receives an attribute query command and transmits, in response to the command, at least one of a module identification, a module location, a description of module memory sizes and types, a module performance parameter and a module serial number.
13. (Original) The circuit array module according to claim 5, further comprising a connector for connecting the processor to an adjoining circuit array module.
14. (Original) The circuit array module according to claim 13, wherein the adjoining circuit array module is able to be connected to at least one additional circuit array module.
15. (Original) The circuit array module according to claim 14, wherein the identity depends on at least one of the adjoining circuit array module and the at least one additional circuit array module.
16. (Original) The circuit array module according to claim 13, wherein the connector conveys at least one of test stimulus data, response data and continuous data output.

17. (Original) The circuit array module according to claim 13, wherein the configuration selector further accepts a configuration command through the connector and wherein the selected configuration definition is selected based at least in part upon the configuration command.
18. (Original) The circuit array module according to claim 13, wherein the configuration storage receives additional configuration definitions for storage from the connector.
19. (Original) The circuit array module according to claim 13, further comprising:
a circuit location detector for receiving circuit location input information from the adjoining circuit array module and producing a circuit location indicator in dependence upon the circuit location input information, and wherein the attribute depends upon the circuit location indicator.
20. (Original) The circuit array module according to claim 19, further comprising at least one additional connector for at least outputting the circuit location output information.
21. (Original) The circuit array module according to claim 13, further comprising at least a second connector for connecting to at least a second adjoining circuit array module.
22. (Original) The circuit array module according to claim 15, wherein at least one signal communicated through the connector is further communicated through at least one of the at least one second connector.

23. (Original) The circuit array module according to claim 16, wherein the connector is located on a first surface of the module and at least one of the at least one second connector is located on a second surface of the module, wherein the second surface is opposite the first surface.

24. (Original) The circuit array module according to claim 21, wherein the configuration selector transmits the attribute through the connector.

25. (Original) The circuit array module according to claim 21, wherein an attribute descriptor is received through the connector and relayed through the second connector.

26. (Original) The circuit array module according to claim 21, wherein at least one signal received through the connector is processed by the processor to produce an output signal, and the output signal is transmitted through the second connector.

27. (Original) The circuit array module according to claim 21, wherein at least one signal is routed from the connector to the second connector, wherein the routing is based at least in part on the selected configuration definition.

Claims 28-37 (Cancelled)